

Appln. No.: Unassigned  
Prelim. Amdt. dated November 19, 2003  
Attorney Docket No. 13549US02

**Amendments to the Specification:**

Please replace the first paragraph of the specification with the following paragraph:

This patent application is a continuation of U.S. Patent Application No. 10/091,099, filed March 5, 2002, which is a continuation of U.S. Patent Application No. 09/568,520, filed May 9, 2000, now U.S. Patent No. 6,389,077, issued May 14, 2002, which claims priority under 35 U.S.C. § 120 of U.S. Patent Application No. 09/399,202, filed September 17, 1999, entitled “ADAPTIVELY CONFIGURABLE CLASS-A/CLASS-B TRANSMIT DAC FOR TRANSCEIVER EMISSION AND POWER CONSUMPTION CONTROL,” and is a continuation of U.S. Patent Application No. 09/429,893, filed October 29, 1999, entitled “INTEGRATED GIGABIT ETHERNET TRANSMITTER ARCHITECTURE,” which claims the benefit of the filing date of United States Provisional Patent Applications Serial Nos. 60/106,265, filed October 30, 1998, and entitled “POWER EFFICIENT AND REDUCED EMI EMISSIONS TRANSMITTER,” [[,]] 60/107,105, filed November 4, 1998, and entitled “GIGABIT ETHERNET TRANSMITTER,” [[,]] 60/107,102, filed November 9, 1998, and entitled “ETHERNET GIGABIT ANALOG SYSTEM,” [[,]] and 60/108,001, filed November 11, 1998, and entitled “ADAPTIVE ELECTRONIC HYBRID LINE DRIVER FOR GIGABIT ETHERNET,” the entire contents of which are hereby expressly incorporated by reference.